AMENDMENTS TO THE CLAIMS

Claim 1 (Currently Amended): In ~~an electronic~~a wireless communication device having a processor, a computer readable memory, and at least one hardware resource coupled to each other, a method of ~~OPERATING~~ operating the hardware resources~~, the method~~ comprising the steps of:

a) locating a first address in the computer readable memory of the wireless communication device, the first address containing operating information associated with a first hardware resource;

b) transmitting operating information associated with the first address to the first hardware resource;

c) reading a pointer associated with the first address that locates a subsequent address for a subsequent hardware resource; and

d) repeating steps a) through c) for a quantity of pointers respectively associated with multiple hardware resources.

Claim 2 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the method further comprises the step of:

e) returning to the first pointer when all of the quantity of pointers has been exhausted in a list stored in memory.

Claim 3 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the method further comprises the step of:

e) repeating steps a) through c) for each of multiple sets of operating information associated with multiple uses of the hardware resource.

Claim 4 (Currently Amended): The ~~electronic~~ wireless communication device recited in claim 3 wherein the multiple sets of operating information are utilized within a system cycle.

Claim 5 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the method further comprises the step of:

e) repeating steps a) through d) for a plurality of entries of operating information for the hardware resource, wherein each of the entries is respectively associated with a reuse of the hardware resource for a different application at a different point in time.

Claim 6 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the information for operating the first hardware resource includes semi-static hardware control parameters.

Claim 7 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 6 wherein the semi-static hardware control parameters include flags, parameters, or states for the first hardware resource.

Claim 8 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the information for operating the first hardware resource includes dynamic hardware control parameters.

Claim ~~10~~ 9 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 8 wherein the dynamic hardware parameters are controlled by dedicated hardware resources.

Claim ~~11~~ 10 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 7 wherein the hardware resources include at least one tracking finger.

Claim ~~12~~ 11 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the hardware resources include at least one searcher element.

Claim ~~13~~ 12 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the hardware resources include at least one downlink transmitter element.

Claim ~~14~~ 13 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein the hardware resources include at least one matched filter element.

Claim ~~15~~ 14 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~15~~ 1 wherein the method further comprises the step of:

e) executing a pointer from a primary list of pointers that transfers control to a secondary list with operating information associated with the hardware resource.

Claim ~~16~~ 15 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim 1 wherein only the hardware resources in the secondary list that are grouped together for a specific category are enabled via the pointer from the primary list.

Claim ~~17~~ 16 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~16~~ 15 wherein the secondary list has a pointer at the end of the operating information grouped together for the specific purpose, the pointer for the secondary list returning control to the primary list.

Claim ~~18~~ 17 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~16~~ 15 wherein the primary list has a plurality of pointers that point to at least one other list that tracks an identification of a user of hardware resources.

Claim ~~19~~ 18 (Currently Amended): In ~~an electronic~~ a wireless communication device having a processor, a computer readable memory, and at least one hardware resource all coupled to each other, a method of generating a scheduler for managing the hardware resource, the method comprising the steps of:

a) receiving at the ~~electronic~~ wireless communication device, a quantity of hardware resources available in the ~~electronic~~ wireless communication device;

b) receiving operation information for the hardware resource; and

c) generating a list in the memory for linking requests for using the hardware resource.

Claim ~~20~~ 19 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~19~~ 18 wherein the method further comprises the steps of:

d) receiving a request from a requester for using the hardware resource in the wireless communication ~~electronic~~ device; and

e) associating operating information for the given hardware resource with the requester in an entry of the list.

Claim ~~21~~ 20 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~19~~ 18 wherein the hardware resources managed by the list have the same function.

Claim ~~22~~ 21 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~19~~ 18 further comprising the step of:

d) generating a memory address that links the operation information of the hardware resources to another hardware resource.

Claim ~~23~~ 22 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~20~~ 19 further comprising the step of:

f) generating a memory address that links a last hardware resource to a first hardware resource.

Claim ~~24~~ 23 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~20~~ 19 further comprising the step of:

f) generating a memory address that links the hardware resources for each of multiple reuses within the given time span.

Claim ~~25~~ 24 (Currently Amended): The wireless communication ~~electronic~~ device recited in claim ~~20~~ 19 further comprising the step of:

f) generating a second list that provides a pointer to operation information of hardware resources that have a common category.

Claim ~~26~~ 25 (Currently Amended): A wireless communication system for communicating information between a host communication device and an external communication device, the system comprising:

a receiver for receiving a request for using a hardware resource in the host communication device for communicating to the external communication device of the wireless communication system;

means for modifying a scheduler for the hardware resources in computer memory of the host communication device to satisfy the request; and

means for operating the hardware resources in the host communication device according to the modified scheduler.

Claim ~~27~~ 26 (Currently Amended): In ~~an electronic~~a wireless communication device having a processor, a means for storing a list of information, and at least one hardware resource coupled to each other, a method of operating the hardware resources~~, the method~~ comprising the steps of:

a) locating a first address in the means for storing a list of information of the wireless communication device, the first address containing operating information associated with a first hardware resource;

b) transmitting operating information associated with the first address to the first hardware resource;

c) reading a pointer associated with the first address that locates a subsequent address for a subsequent hardware resource; and

d) repeating steps a) through c) for a quantity of pointers respectively associated with multiple hardware resources.